## Notice of References Cited

Application/Control No.

10/788,725

Examiner

Hong C. Kim

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Reexamination
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Art Unit
2185

Page 1 of 1

## U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-2002/0144101	10-2002	Wang et al.	712/240
*	В	US-2004/0015675	01-2004	Kyker et al.	711/207
*	С	US-2004/0088489	05-2004	Hironaka et al.	711/131
*	D	US-2005/0125613	06-2005	Kim et al.	711/125
*	Е	US-2005/0132169	06-2005	Mates, John W.	712/208
*	F	US-6,170,038	01-2001	Krick et al.	711/125
	G	US-			
	Ι	US-			
	_	US-			
	٦	US-			
	K	US-			
	L	US-			
	М	US-			

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	σ	10 TO 1000				
	R					
	s					
	T					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Rotenberg, E.; Bennett, S.; Smith, J.E., Trace cache: a low latency approach to high bandwidth instruction fetching, Microarchitecture, 1996. MICRO-29. Proceedings of the 29th Annual IEEE/ACM International Symposium on ,1996, Pagets) 24 - 34
	٧	
	w	
	x	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

U.S. Patent and Trademark Office PTO-892 (Rev. 01-2001)

**Notice of References Cited** 

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Part of Paper No. 20060219